

Figura 1

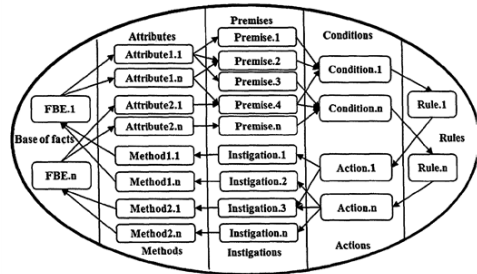


Figura 2

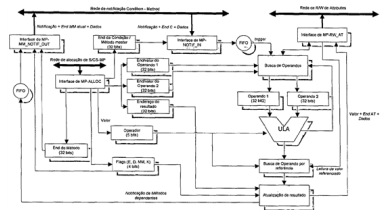


Figura 16



Figura 17

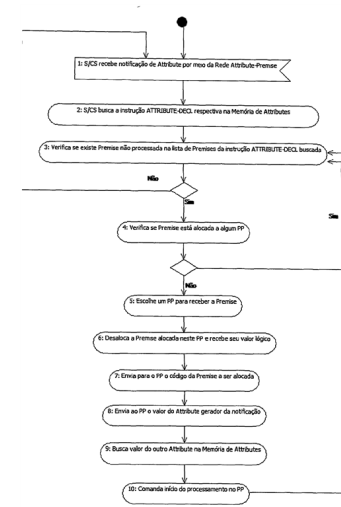


Figura 18

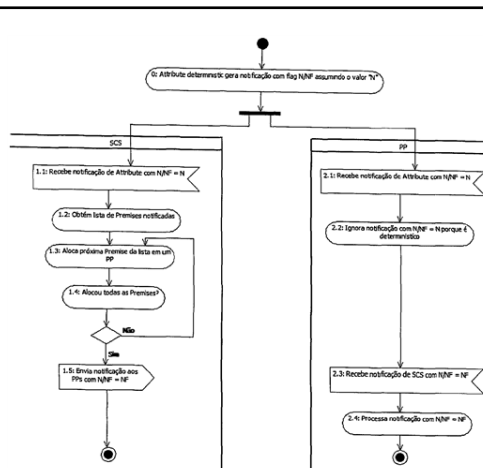


Figura 20

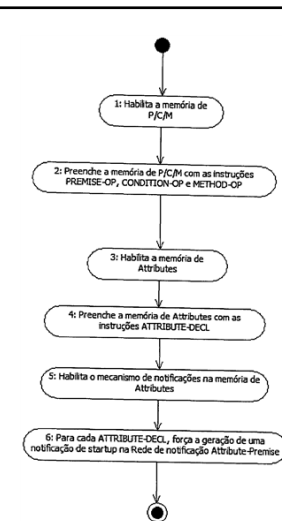


Figura 21

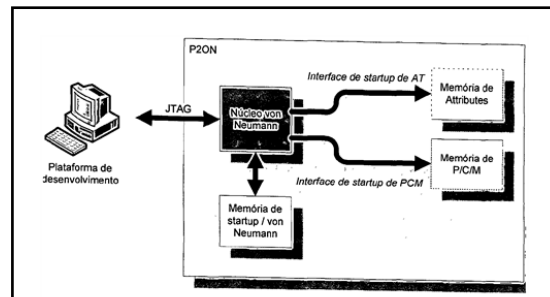


Figura 22

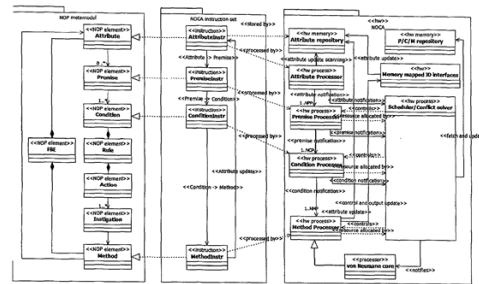


Figura 3

Mnemônico:
ATTRIBUTE-DECL, [E], [D], [N], [R], NP, Value, [AP₀, AP₁, ..., AP_{NP-1}]

Codificação:	3	1	1	1	1	9	16	32	NP*32
001	E	D	N	R	Reserved	NP	Value	AP ₀	AP ₁ AP _{NP-1}

Figura 4

Mnemônico: PREMISE-OP, [E], [D], [R], [I1], [I2], [A/V], Op, NC, A/V AT1, A/V AT2, [AC ₀ , AC ₁ , ..., AC _{NC-1}]													
Codificação:													
2	1	1	1	1	4	2	1	1	1	1	7	8	
00	E	D	R	I1	I2	Op	A/V	V1	V2	LR	INI	Reserved	NC

32			32			NC*32					
A/V AT1			A/V AT2			AC ₀			AC _{NC-1}		

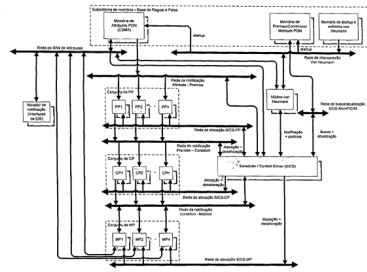


Figura 12

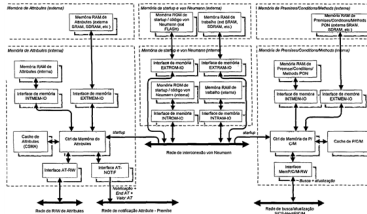


Figura 13

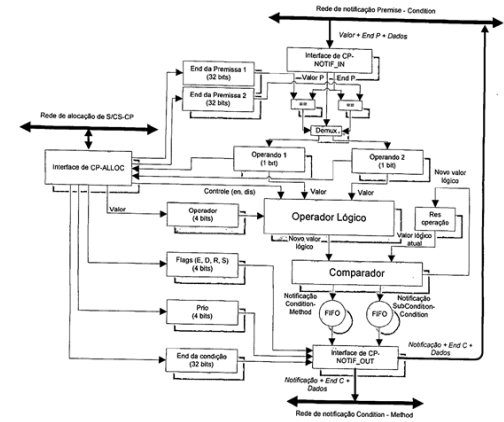


Figura 15